

# Challenges and Opportunities in Design and Operation of Intelligent Cyber-Physical Systems

Invited Talk, 19<sup>th</sup> International Conference on Runtime Verification (RV) Part of the 3<sup>rd</sup> World Congress on Formal Methods Porto, Portugal. October 10, 2019

Akshay Rajhans, PhD arajhans@mathworks.com https://arajhans.github.io





#### Talk outline

- > Cyber-physical systems: a feature classification
- "Runtime" verification at design time: simulation as a proxy for run time
- Runtime analysis at operation time: From CPS to IoT and Digital Twins
- Challenges and future outlook

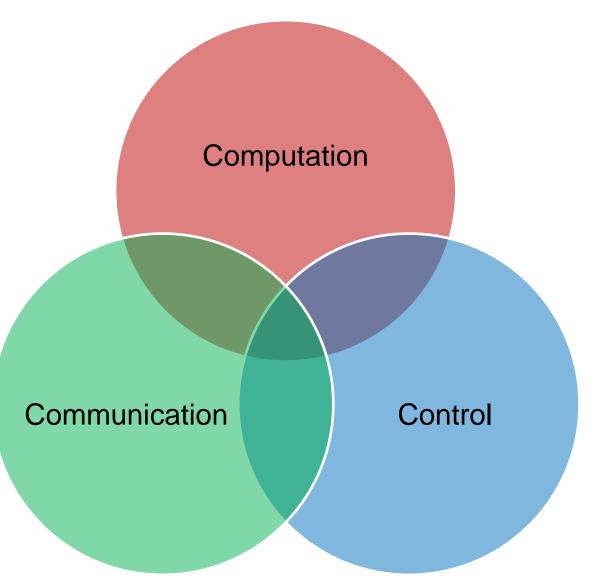


### Cyber-physical systems: umbrella term, not a precise definition

"The term cyber-physical systems (CPS) refers to a new generation of systems with *integrated computational and physical* capabilities that can *interact with humans* through many new modalities.

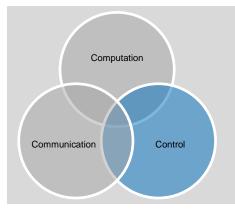
The ability to interact with, and expand the capabilities of, the physical world through *computation*, *communication*, and *control* is a key enabler for future technology developments."

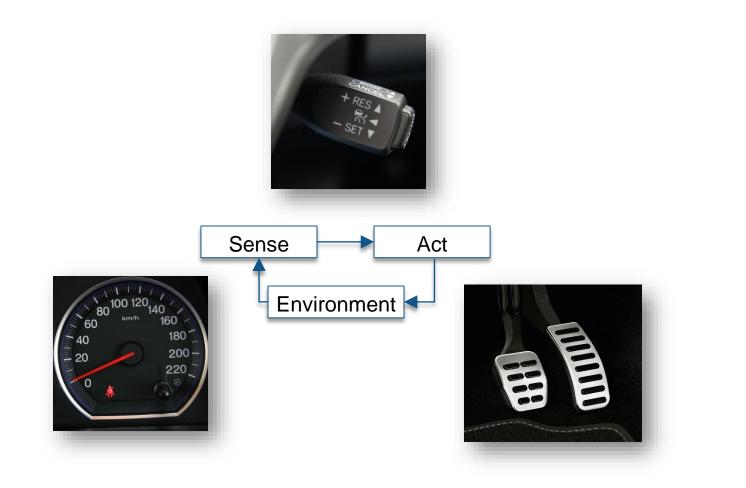
- Helen Gill and Kisan Baheti, NSF IEEE Impact of Control Technology, T. Samad and A.M. Annaswamy (eds.), 2011. Available at www.ieeecss.org.





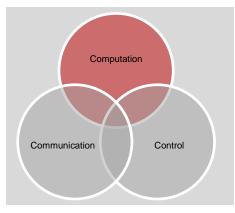
### Control – closing the loop over the physical environment

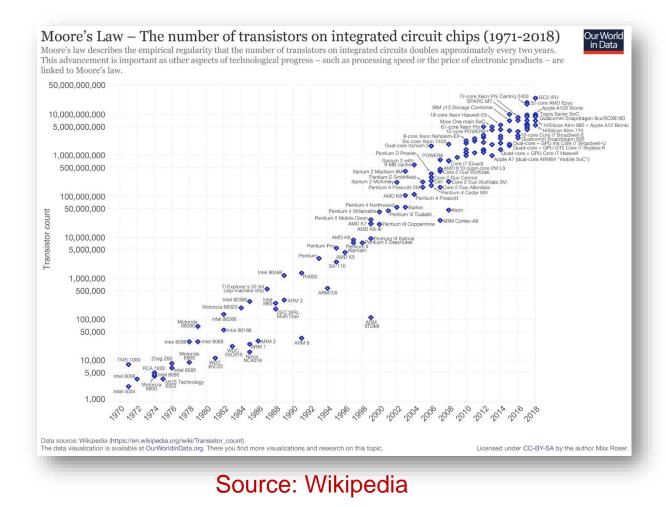






#### Computation – fueled by Moore's law

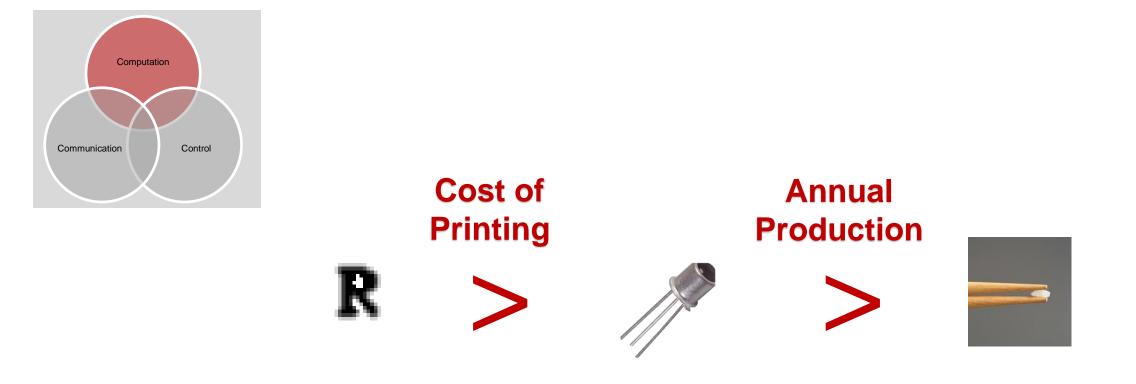




**Computation Axis** 



#### Computation – fueled by Moore's law

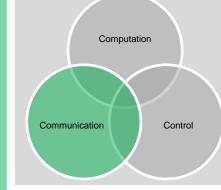


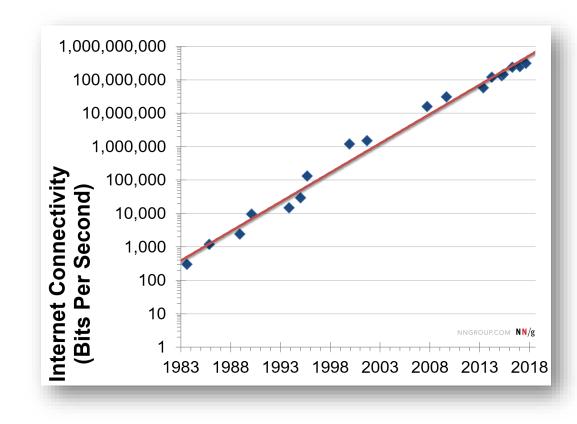
Source: Intel, IEEE

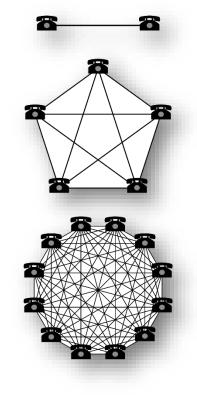
**Computation Axis** 



#### Communication – Nielsen's law, Metcalfe's law





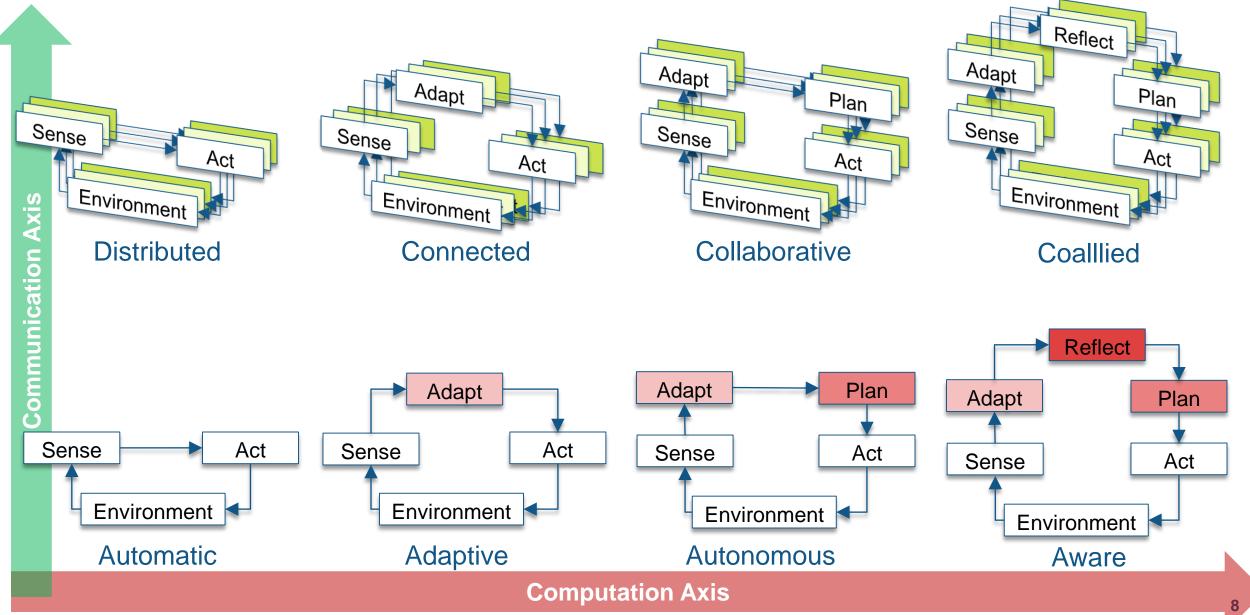


Source: nngroup.com

Source: Wikipedia



#### **CPS** feature classification





#### References

Configuration	Behavior						
	Reflexive	Reactive	Reasoned	Reflective			
Individual	Automatic	Adaptive	Autonomous	Aware			
Ensemble	Distributed	Connected	Collaborative	Coallied			

[CMR<sup>+</sup>20] S. Castro, P. Mosterman, A. Rajhans, R. Valenti, "*Challenges in the Operation and Design of Intelligent Cyber-Physical Systems*", to appear.

[ABK<sup>+</sup>19] F. Allgöwer, J. Borges de Sousa, J. Kapinski, P. Mosterman, J. Oehlerking, P. Panciatici, M. Prandini, A. Rajhans, P. Tabuada, P. Wenzelburger, "*Position paper on the challenges posed by modern applications to cyber-physical systems theory*", Nonlinear Analysis: Hybrid Systems, Volume 34, Pages 147-165, November 2019.

[TBD<sup>+</sup>18] A. Tolk, F. Barros, A. D'Ambrogio, A. Rajhans, P. J. Mosterman, S. S. Shetty, M. K. Traoré, H. Vangheluwe, and L. Yilmaz, "*Hybrid Simulation for Cyber Physical Systems – A Panel on Where are we Going Regarding Complexity, Intelligence, and Adaptability of CPS Using Simulation*," Spring Simulation Multi-Conference, 2018.

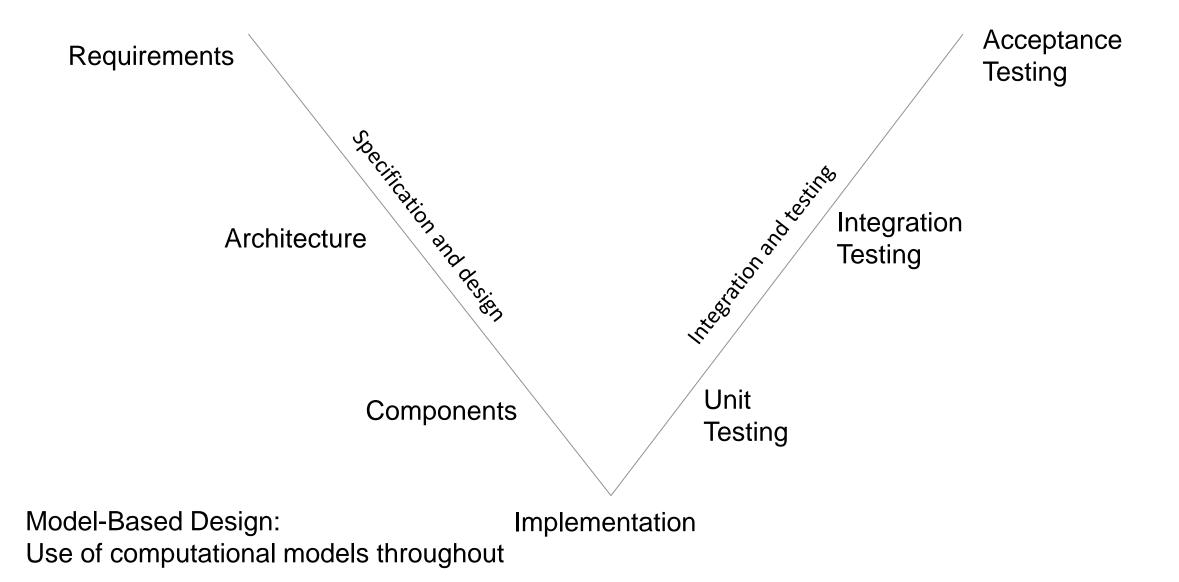


#### Outline

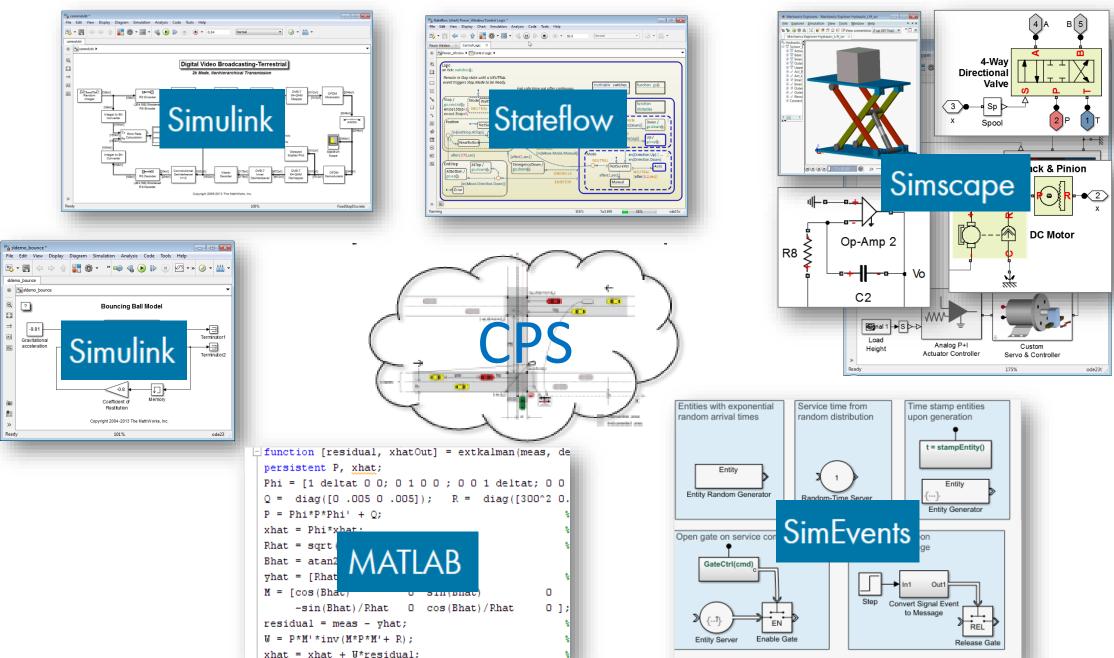
- CPS feature classification
- > "Runtime" verification at design time: simulation as a proxy for run time
- From CPS to IoT and Digital Twins: runtime analysis
- Challenges and future outlook



#### Model-Based Design: Models as a proxy for the real system







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### Simulations for increasingly faithful proxies of runtime behavior

- Model-in-the-loop simulation
- Software-in-the-loop simulation
- Processor-in-the-loop simulation
- Hardware-in-the-loop simulation
- Gaming-engine-in-the-loop

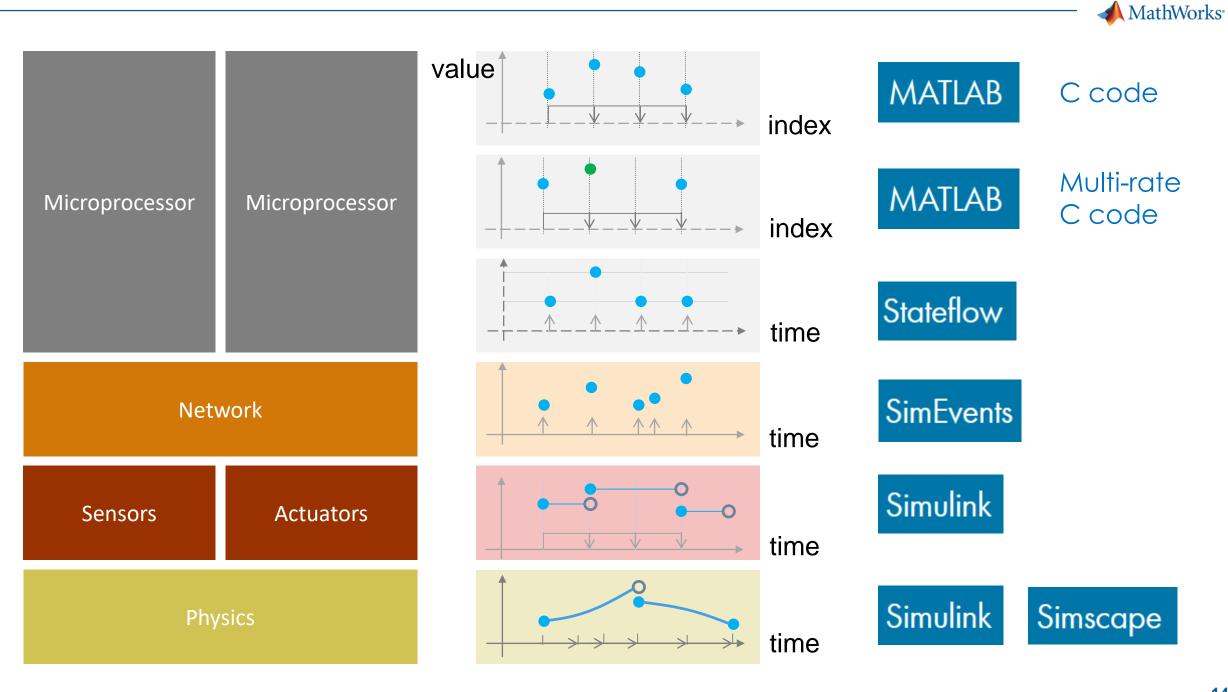
simulate / test the model the generated code code on the processor plant on real-time h/w visualization, physics



Ride & handling

Chassis controls

ADAS / AD





#### Not today – how to address heterogeneity formally?

[MRM<sup>+</sup>20] P.J. Mosterman et al., "*Simulation of Hybrid Dynamic Systems*", Springer Encyclopedia of Systems and Control, Second Edition, to appear.

[RBR+14] A. Rajhans et al., "Supporting Heterogeneity in Cyber-Physical System Architectures", IEEE Transactions on Automatic Control, Special Issue on Control of CPS, Vol. 59, Issue 12, pages 3178-3193.

[R13] A. Rajhans, "*Multi-Model Heterogeneous Verification of Cyber-Physical Systems*," **PhD Thesis**, Carnegie Mellon University, 2013.

[RK13] A. Rajhans and B. H. Krogh, "*Compositional Heterogeneous Abstraction*," 16th ACM International Conference on HSCC, 2013.

[RK12] A. Rajhans and B. H. Krogh, "*Heterogeneous Verification of Cyber-Physical Systems Using Behavior Relations*," 15th ACM International Conference on HSCC, 2012.

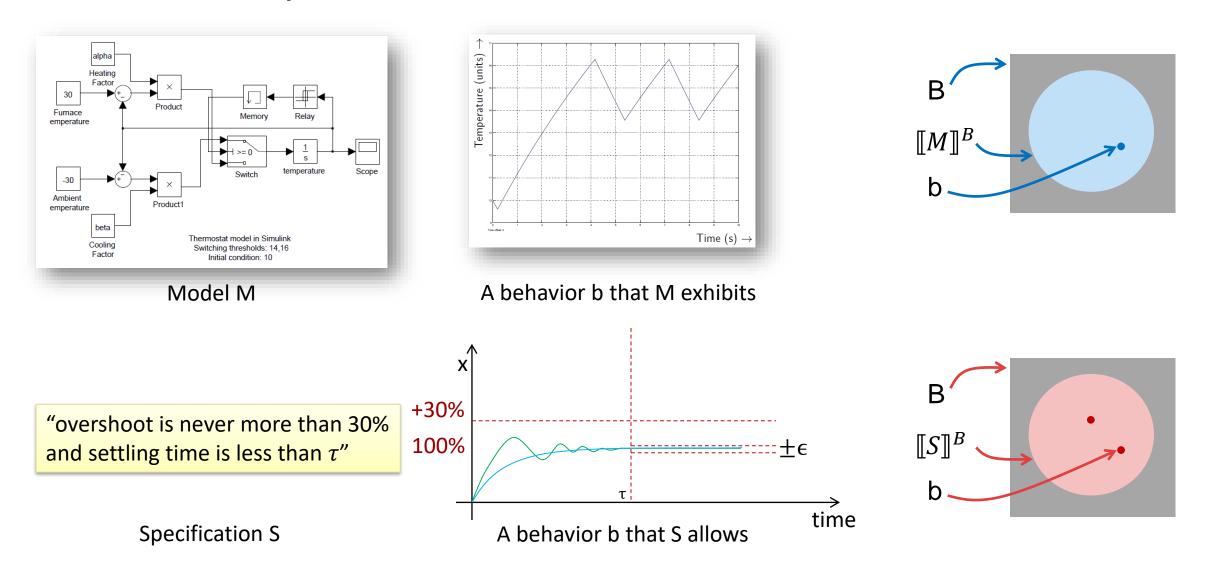
[RBL+11] A. Rajhans et al., "Using Parameters in Architectural Views to Support Heterogeneous Design and Verification," 50th IEEE CDC, 2011.

[BDK<sup>+</sup>10b] A. Bhave et al., "*Augmenting Software Architectures with Physical Components*," Embedded Real Time Software and Systems (ERTS<sup>2</sup>), 2010.

[RCS+09] A. Rajhans et al., "An Architectural Approach to the Design and Analysis of Cyber-Physical Systems," Third International Workshop on Multi-Paradigm Modeling (MPM), 2009.

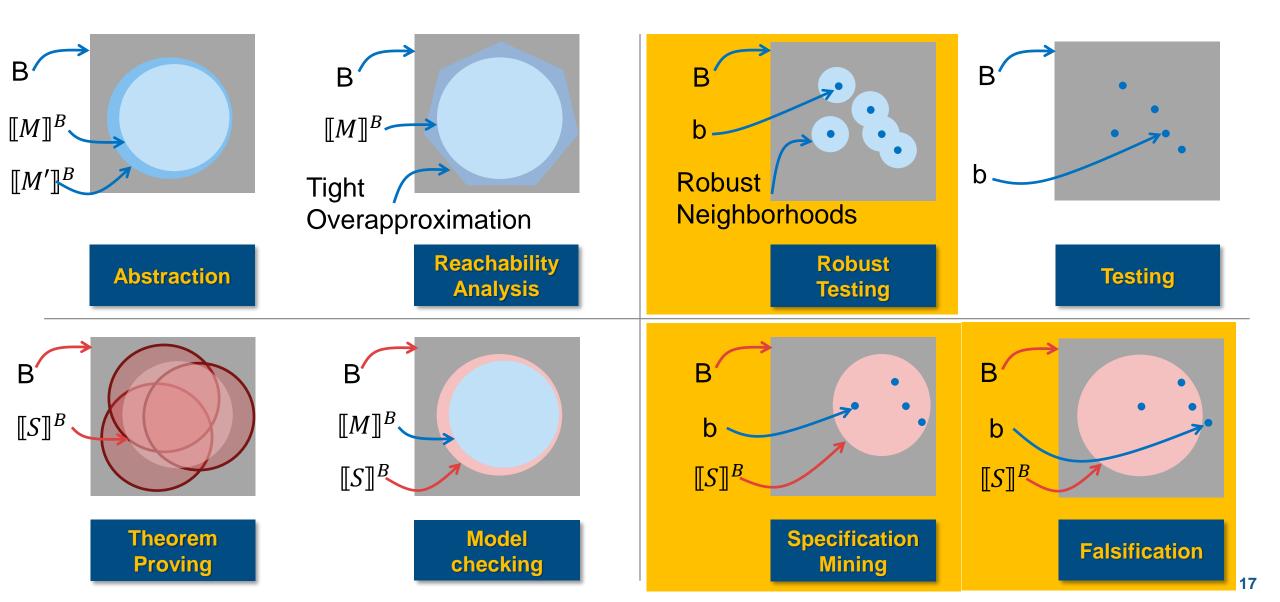


#### **Models and Specifications**



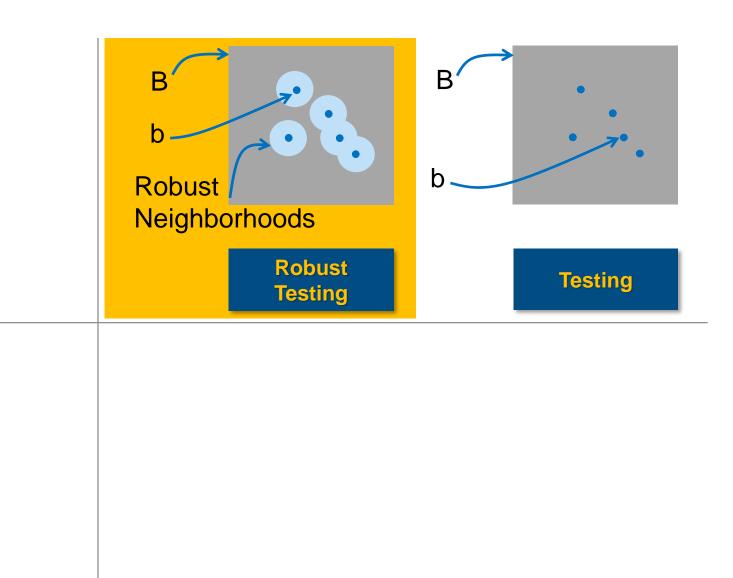


#### Various verification problems





### Robust testing a.k.a. simulation-based reachability analysis





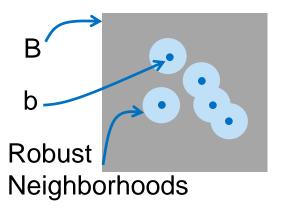
#### Robust testing a.k.a. simulation-based reachability analysis

[R07] A. Rajhans, "*Development of Robust Testing Toolbox for Hybrid Systems*," MS Thesis, University of Pennsylvania, 2007.

[DRJ13] Y. Deng, A. Rajhans, and A. A. Julius, "STRONG: A Trajectory-Based Verification Toolbox for Hybrid Systems," 10th International Conference on Quantitative Evaluation of SysTems (QEST), 2013.

[DKR09] A. Donzé, B. H. Krogh, and A. Rajhans, "Parameter Synthesis for Hybrid Systems with an Application to Simulink Models," 12th IEEE/ACM International Conference on Hybrid Systems: Computation and Control, 2009.





Sensitivity analysis

Related work by Fainekos, Pappas, Balkan, Tabuada, Zutshi, Sankaranarayanan, Kanade, Alur, ...

Lyapunov analysis, contraction metrics, barrier certificates, concolic testing ...



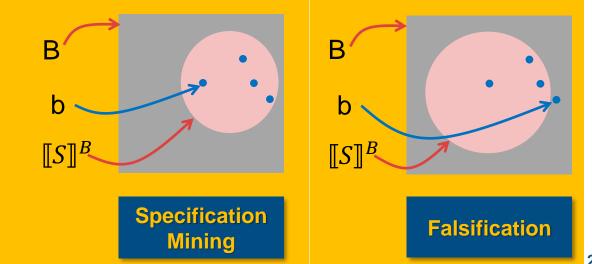
Toyota adoption a success story

# Simulation-Based Approaches for Verification of Embedded Control Systems

JAMES KAPINSKI, JYOTIRMOY V. DESHMUKH, XIAOQING JIN, HISAHIRO ITO, and KEN BUTTS

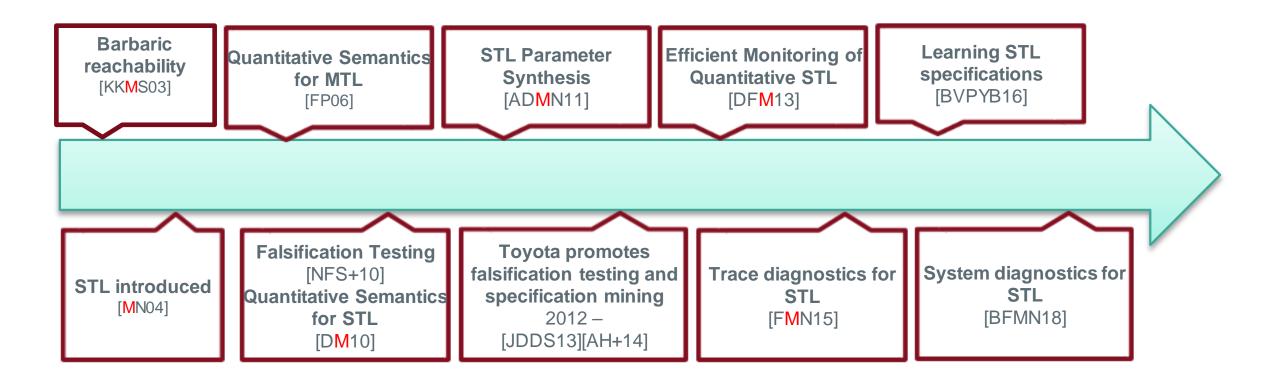


Formalizing specifications to enable falsification





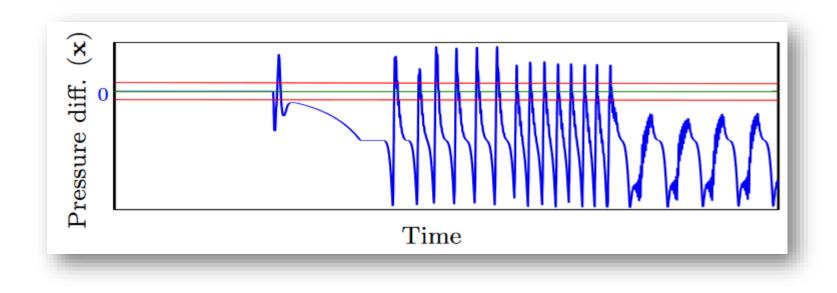
#### Signal Temporal Logic as a success story



Credit: Dejan Ničković (via Bruce Krogh), Oded Maler: A memory box full of diamonds, MT-CPS 2019.



#### An actual bug uncovered via falsification at Toyota



#### Mining Requirements from Closed-Loop Control Models

Xiaoqing Jin Univ. of California Riverside jinx@cs.ucr.edu Alexandre Donzé Univ. of California Berkeley donze@eecs.berkeley.edu

Sanjit A. Seshia Univ. of California Berkeley sseshia@eecs.berkeley.edu Jyotirmoy V. Deshmukh Toyota Technical Center jyotirmoy.deshmukh@tema.toyota.com



### Considerations for engineering adoption of temporal logics

- Engineers are not logicians logic vocabulary could be a challenge
- Simple engineering concepts may require complex logical formulas
- Multiple modeling formalisms that interact
- Multiple combinations of time/signal domains, data types, solver settings

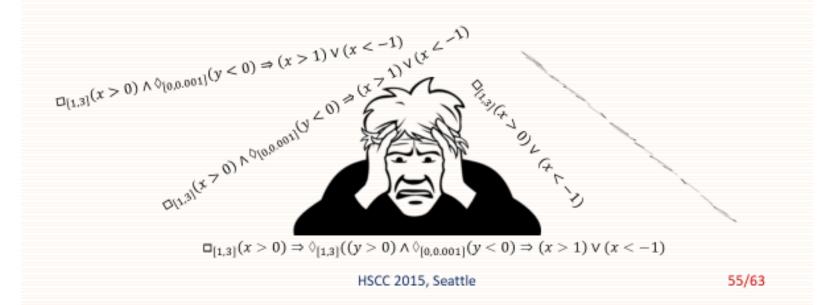


#### HSCC 2015 Keynote, Jyotirmoy Deshmukh, (then) Toyota

Automotive Industry Trends MBD Verification Techniques Challenges

Grand Challenge I: Requirement Engineering

- Key challenge for Toyota, Bosch, and others
- How do you present requirements to control designers?
- How do they convey their intention without using formalisms?
- Is Temporal Logic the right requirement language?



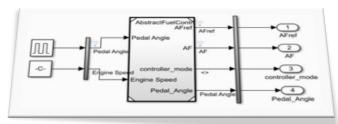


### Logical and temporal assessments in Simulink Test

• Formalize and execute requirements directly as Test Assessments



- 1. The difference between the room temperature and the set temperature should never exceed 6 degrees.
- 2. If the temperature difference exceeds 4 degrees for more than 2 seconds, then the pump shall activate for at least 2 seconds



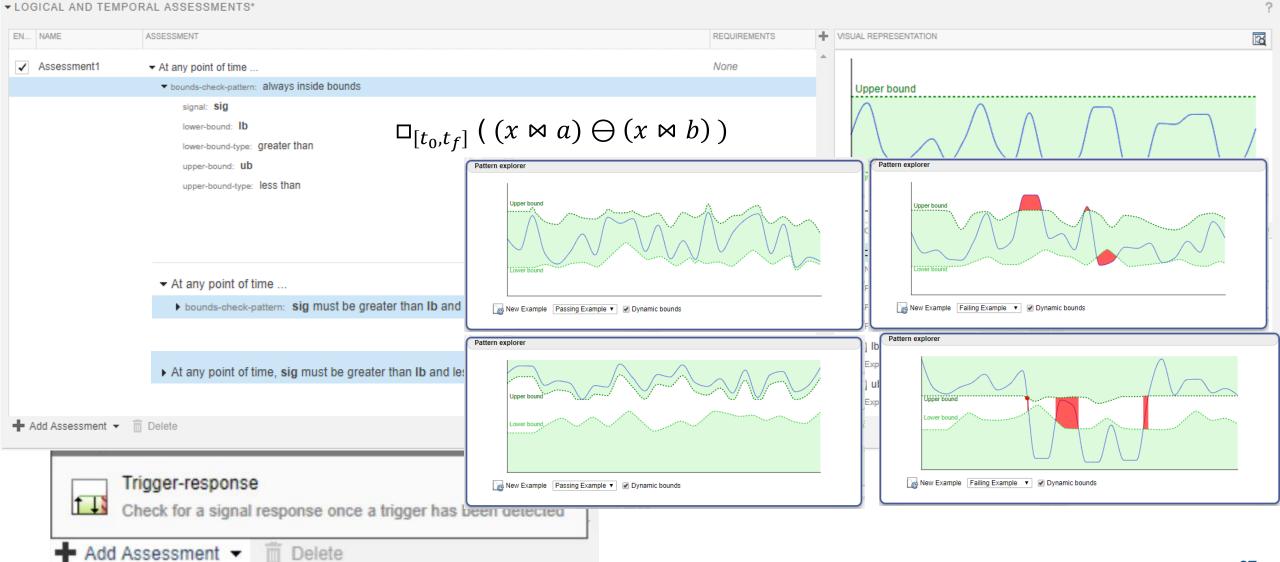
System Under Test

Test Manager     TESTS							XXXXXXX	(M) M)
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Filter tests by name or	tags, e.g. tags: test		ATON SETTINGS OVERNIDES					
▼ 📑 Fuel Control Test	S*	► ITERATION	NS*					
→ C AF tests		▼LOGICAL /	AND TEMPORAL ASSESSMENTS*					
TC1		NAME	ASSESSMENT			+ vis	SUAL REPRESENTATION	
		Assessment1	At any point of time, abs(roomTemperate temperatureTolerance	ture - setTemperatur	re) must be less than	Ť		
		Assessment2	<ul> <li>At any point of time, if abs(roomTempera stays true for at least 2 seconds then, startin proceed and the table to be a starting to be a starting and another the starting to be a st</li></ul>	ing from end of min-ti		d	faise At trigger-mi	
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	d execute	Assessment2	stays true for at least 2 seconds then, starting	ing from end of min-ti		S ,	At trigger-m RESP bymbols tricom Temperature tricom Temperature tricom Temperature tentre Tolerance	false
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PROPERTY	VALUE	Assessment2	stays true for at least 2 seconds then, starting	ing from end of min-ti		S ,	At trigger-m RESP bymbols tricom Temperature tricom Temperature tricom Temperature tentre Tolerance	false
PROPERTY Name	VALUE	Assessment2	stays true for at least 2 seconds then, starting	ing from end of min-ti		S ,	At trigger-m RESP bymbols tricom Temperature tricom Temperature tricom Temperature tentre Tolerance	nin-time trué PONSE false 
РПОРЕНТУ Name Туре	VALUE TC1 Simulation Test	Assessment2	stays true for at least 2 seconds then, starting	ing from end of min-ti		S ,	At trigger-m RESP bymbols tricom Temperature tricom Temperature tricom Temperature tentre Tolerance	nin-time true PONSE false min-t
PROPERTY Name Type Model	VALUE D TC1 Simulation Test AbstractFuelControl		stays true for at least 2 seconds then, starti pumpCmd must stay true for at least 2 seco	ing from end of min-ti		S ,	At trigger-m At trigger-m RESP tymbols torm Temperature torm Temperature to Enformerature Temperature Tolerance to pumpCmd	Ponse faile
PROPERTY Name Type Model Harness Name	VALUE TC1 Simulation Test AbstractFuelControl AbstractFuelControl_Harm		stays true for at least 2 seconds then, starting	ing from end of min-ti		S ,	At trigger-m At trigger-m RESP tymbols torm Temperature torm Temperature to Enformerature Temperature Tolerance to pumpCmd	false



# Authoring

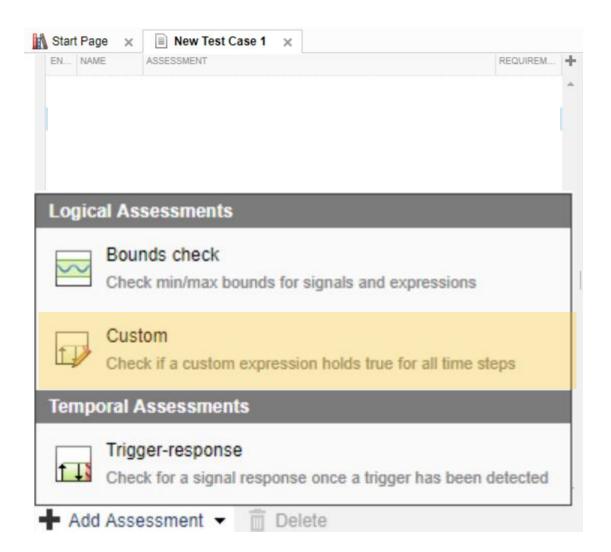
#### ✓ cl+ac+man ✓ LOGICAL AND TEMPORAL ASSESSMENTS\*

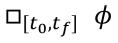




# Authoring

#### >> sltestmgr

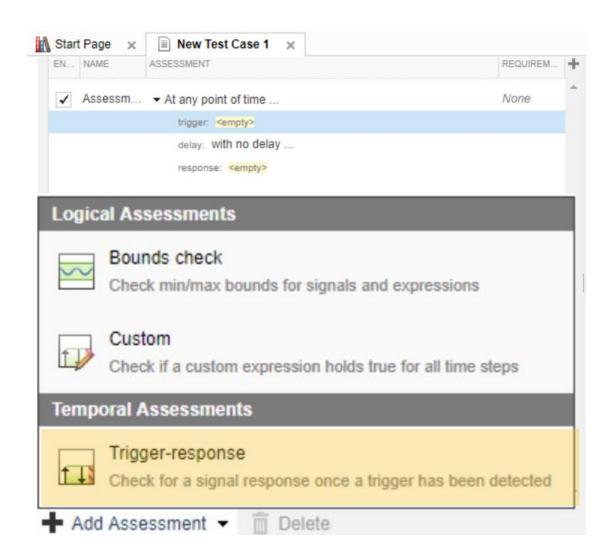






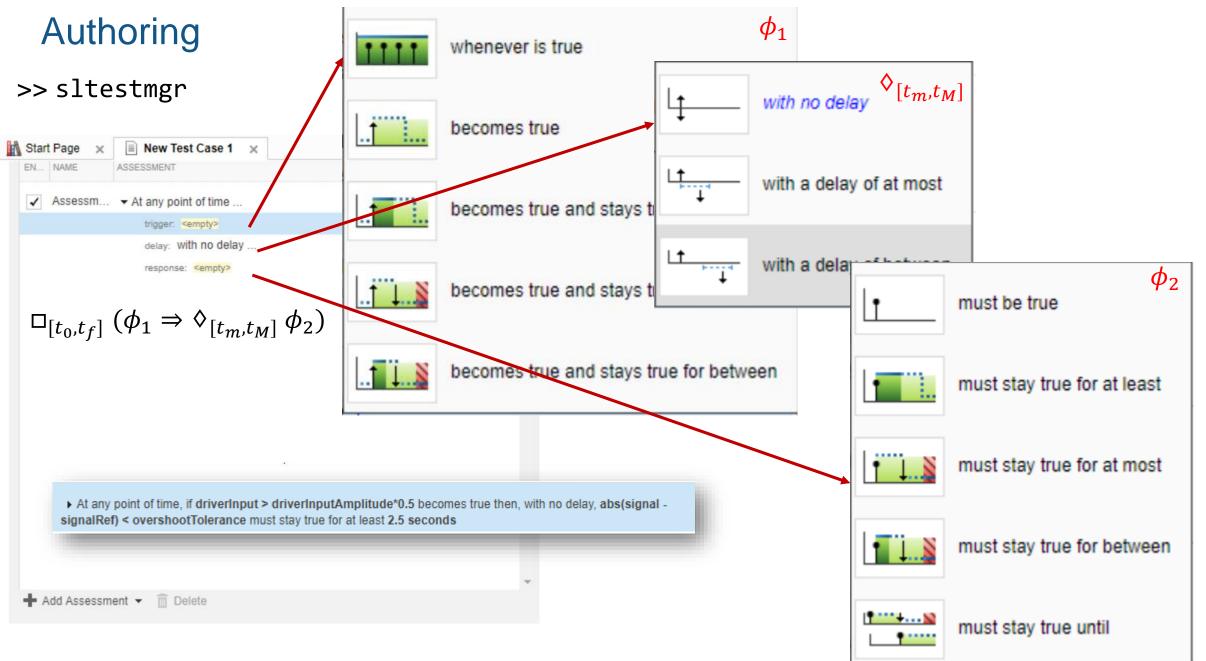
## Authoring

#### >> sltestmgr



 $\Box_{[t_0,t_f]} (\phi_1 \Rightarrow \diamond_{[t_m,t_M]} \phi_2)$ 



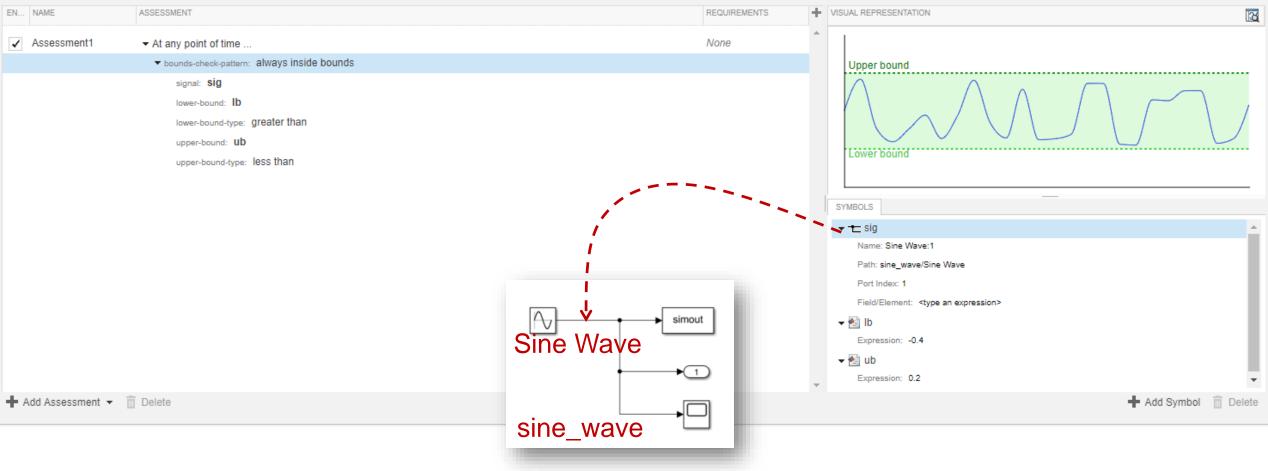


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### Symbol resolution and mapping

#### LOGICAL AND TEMPORAL ASSESSMENTS\*

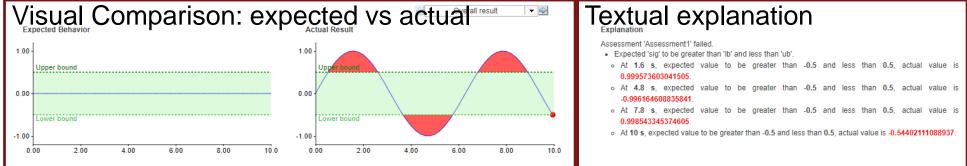


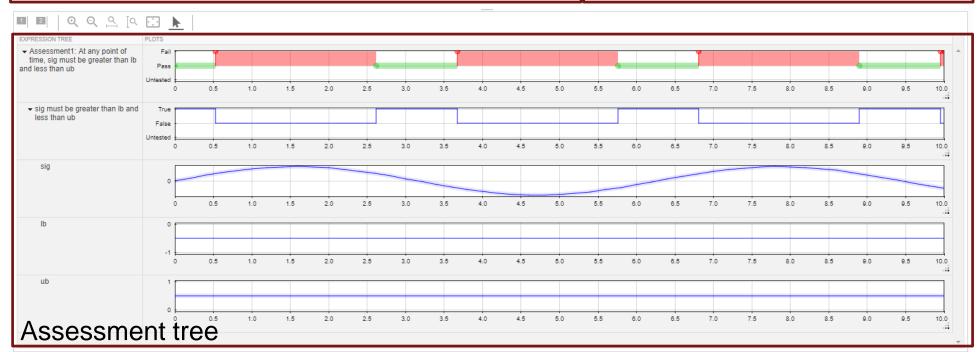


### Assessment and explanation in case of failure

#### Assessment1





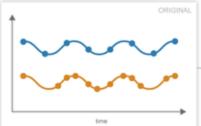


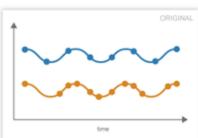
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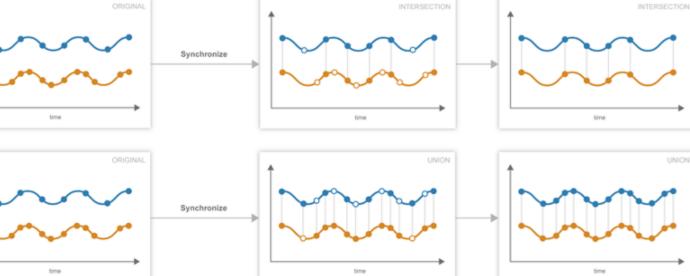
#### **Expression tree** $\Box_{[t_0,t_f]} (\phi_1 \Rightarrow \diamond_{[0,t_M]} \phi_2)$ **Tested and failed** EXPRESSION TREE PLOTS Assessment1: At any point in Fail time, if (abs(mag) > 0.5) Pass becomes true then, with a delay of at most 1.5 seconds, (abs(mag) < Untested 0.1) must stay true for at least 1 seconds if (abs(mag) > 0.5) becomes True true then, with a delay of at $(\phi_1 \Rightarrow \phi_2) \equiv \neg \phi_1 \lor \phi_2 ?$ False most 1.5 seconds, (abs(mag) < 0.1) ш must stay true for at least 1 seconds Untested True **Rising edge** true False Untested (abs(mag) > 0.5) True False Untested with a delay of at most 1.5 True seconds, (abs(mag) < 0.1)False must stay true for at least 1 seconds $[t,T] \oplus [0,1.5]$ Untested - 12 (abs(mag) < 0.1) must stay</li> True true for at least 1 seconds False $[t,T] \ominus [0,1]$ Untested (abs(mag) < 0.1)</p> True **Did not get** False tested Untested



#### Synchronization and interpolation

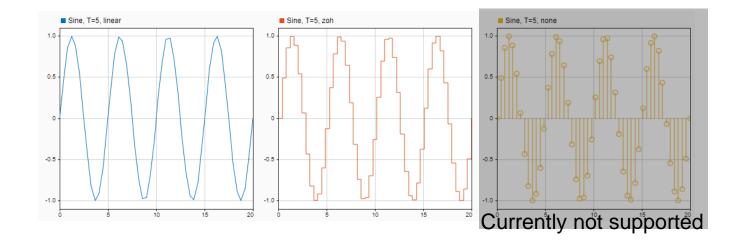






#### Research challenge: heterogeneity

- discrete and continuous time 0
- discrete and continuous value 0 STL  $\heartsuit$  LTL ?
- Needing to up/down-sample may impact frequency domain characteristics
- Dataflow domain: cannot insert 0 or remove data points





#### References

- [DMN19] A. Donzé and A. Rajhans, "Tools Perspective", J. V. Deshmukh, O. Maler, and D. Nickovic, eds., "Specification Formalisms for Modern Cyber-Physical Systems (Dagstuhl Seminar 19071)", 2019.
- [KKR19] J.-F. Kempf, Khoo Y. P., and A. Rajhans, "Specification and Assessment of Temporal Requirements using Simulink Test", Fourth International Workshop on Monitoring and Testing of Cyber-Physical Systems (MT-CPS 2019), part of CPS-IoT Week 2019.
- [ABB+18] S. Anderson, et al., "On the Use of Modeling and Simulation in Robotics," Workshop Report, NIST/NSF/DoD Workshop on Simulation and Machine Learning in Robotics, 2018.
- [DRJ13] Y. Deng, A. Rajhans, and A. A. Julius, "STRONG: A Trajectory-Based Verification Toolbox for Hybrid Systems," 10th International Conference on Quantitative Evaluation of SysTems (QEST), 2013.
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- [R07] A. Rajhans, "Development of Robust Testing Toolbox for Hybrid Systems," MSE Thesis, University of Pennsylvania, 2007.

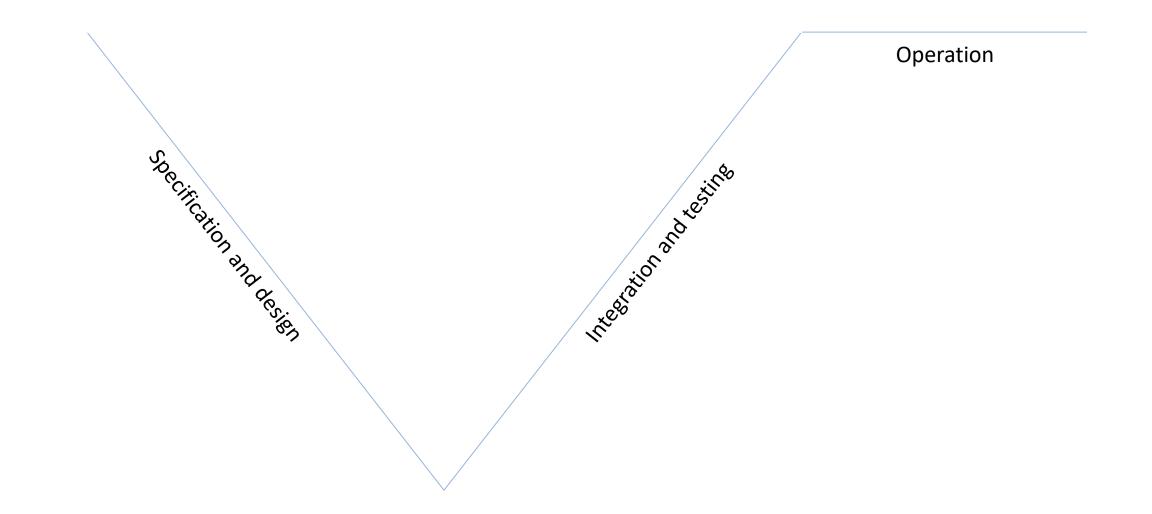


#### Outline

- Cyber-physical systems: a feature classification
- "Runtime" verification at design time: simulation-based approaches
- > Runtime analysis at operation time: From CPS to IoT and Digital Twins
- Challenges and future outlook



## Models are useful in both design and operation



Challenges in the Operation and Design of Intelligent Cyber-Physical Systems, S. Castro, P.J. Mosterman, A.H. Rajhans, and R.G. Valenti, book chapter, *Complexity Challenges in Cyber Physical Systems: Using Modeling and Simulation (M&S) to Support Intelligence, Adaptation and Autonomy*, S. Mittal and A. Tolk, eds., Wiley, 2019.



# Internet of Things topology



Thing







### Internet of Things topology





mytoaster @mytoaster

Social Networking for your Toaster, The Internet of Things (#iot) powered by @ThingSpeak, created by @scharler

Pittsburgh, PA

𝔗 nothans.com/social-network...

III Joined December 2008

#### Representation Photos and videos



CheerLights @cheerlights

CheerLights is an #internetofthings project by @scharler to synchronize lights to the same color at the same time all around the world. #iot #thingspeak

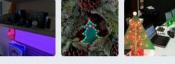
Pittsburgh, PA

𝔗 cheerlights.com

Joined November 2011

#### 35 Photos and videos







Chanklinkte

Following

455

Tweets

1,055

Followers

1,203

Likes

1,284

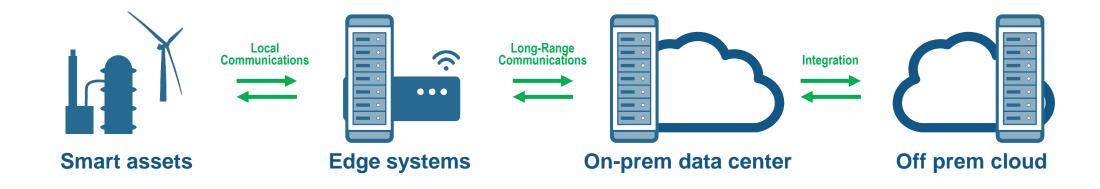


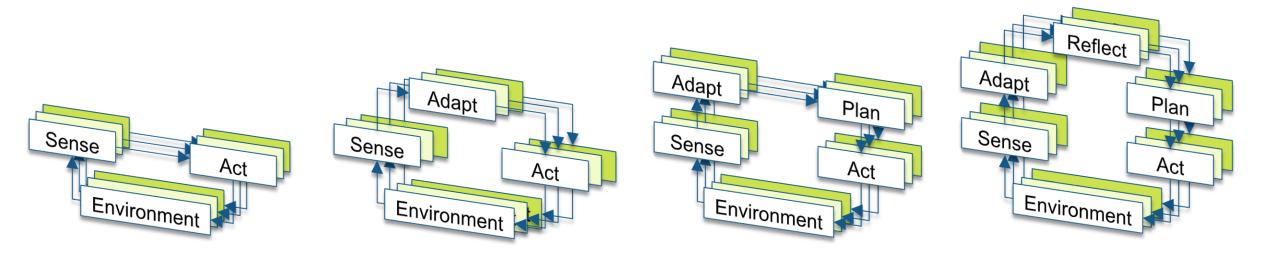
Internet





# Industrial Internet of Things topology – Enterprise level operations

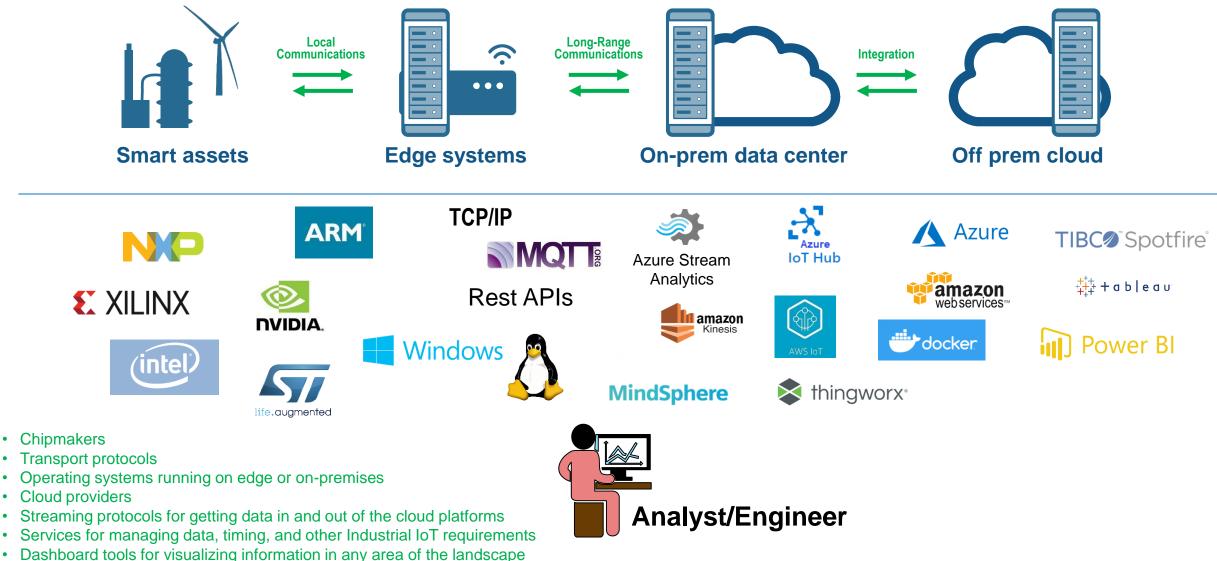




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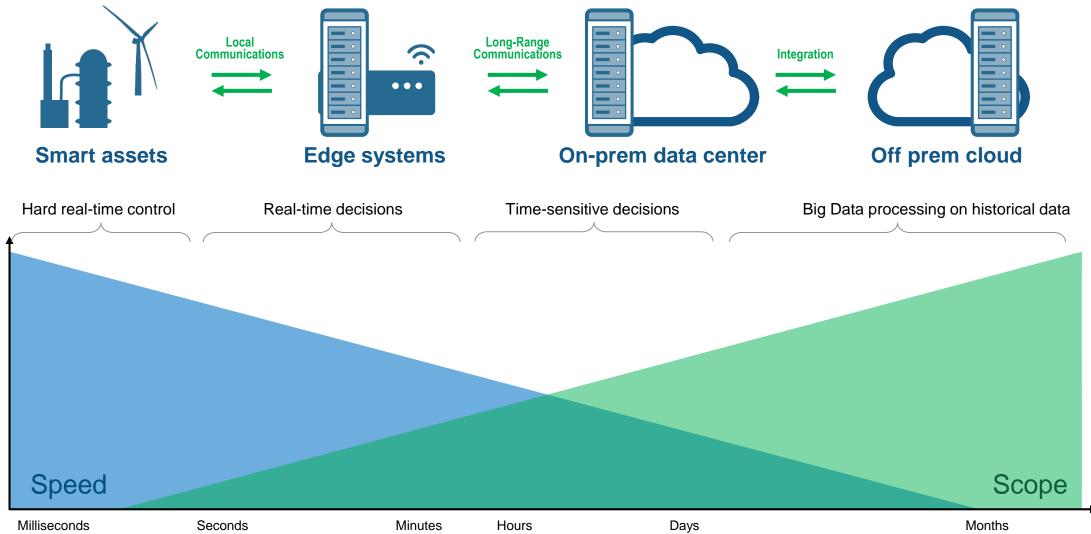
# A complex collection of tools, platforms, and protocols





### Applications at the Asset, the Edge, or Operational Technology Platform

Value of data to decision making

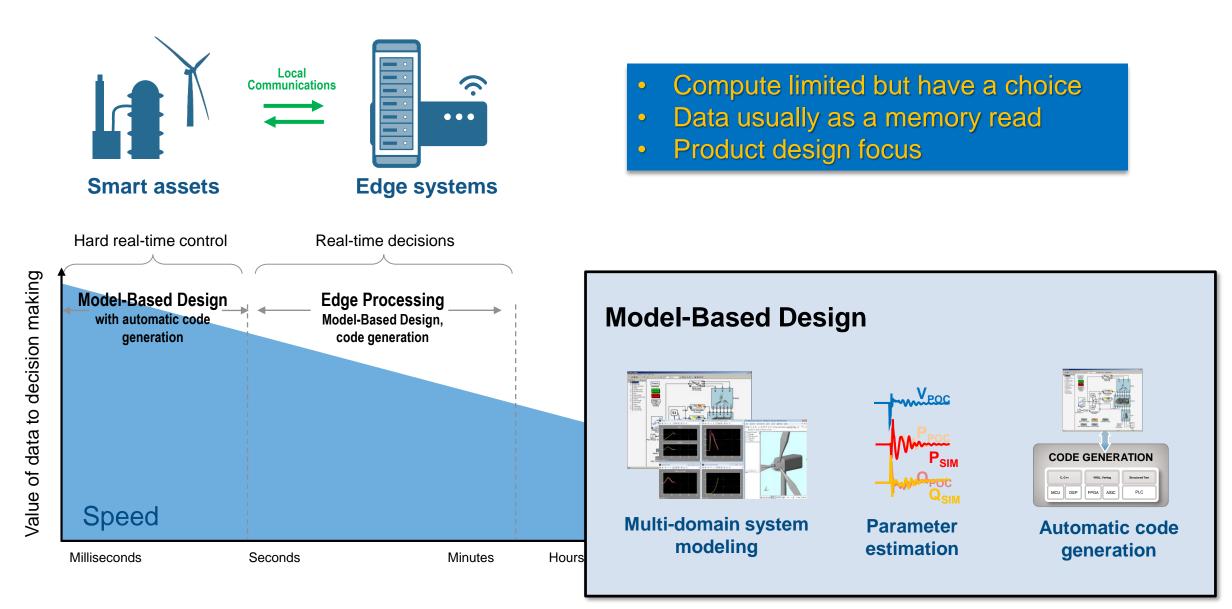


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Time



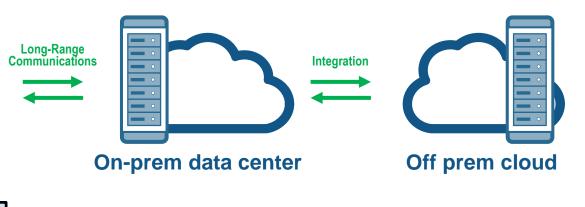
# **Development for Fast and Highly-Deterministic Systems**

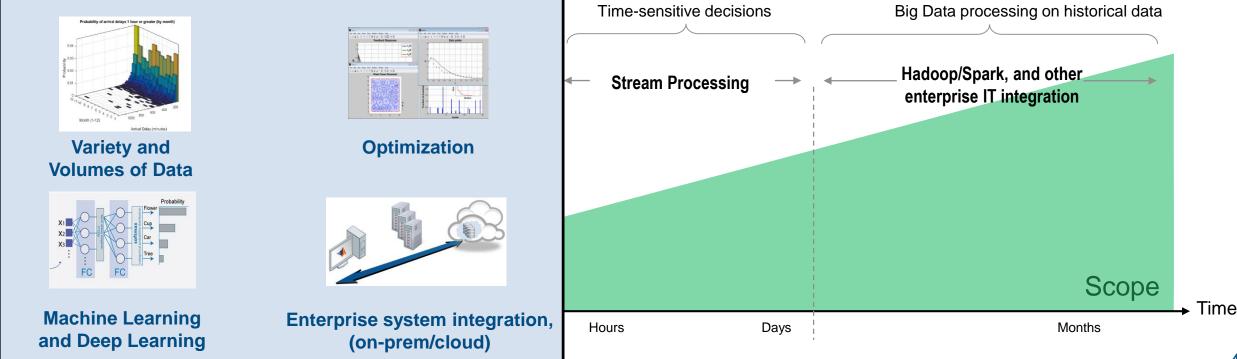




# Development to OT/IT On-Prem and in Cloud

- Compute abundant but less control
- Data access as streaming messages
- Service focus





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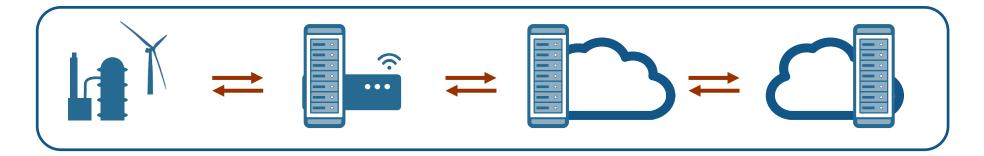


Other Clouds

https://www.mathworks.com/cloud.html



# **Digital Twin**





Create computational model of asset in operation

• Data-driven (MATLAB) or first-principles (Simulink) models

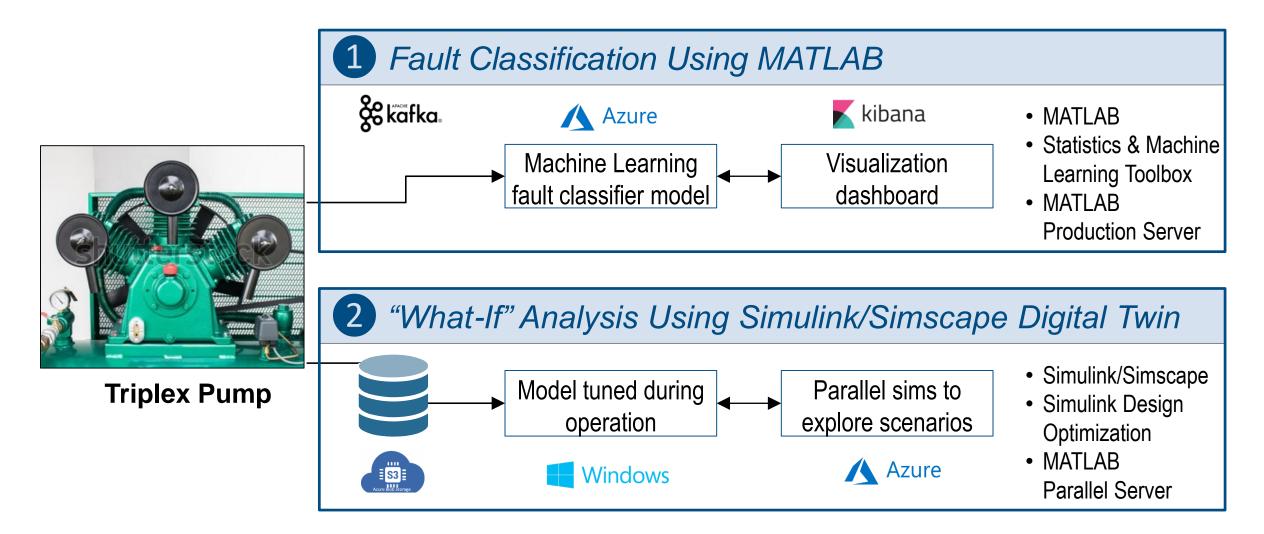


- Reuse models from development process (e.g. MBD)
- Kept up-to-date during asset operation (e.g. aging, wear, environment)

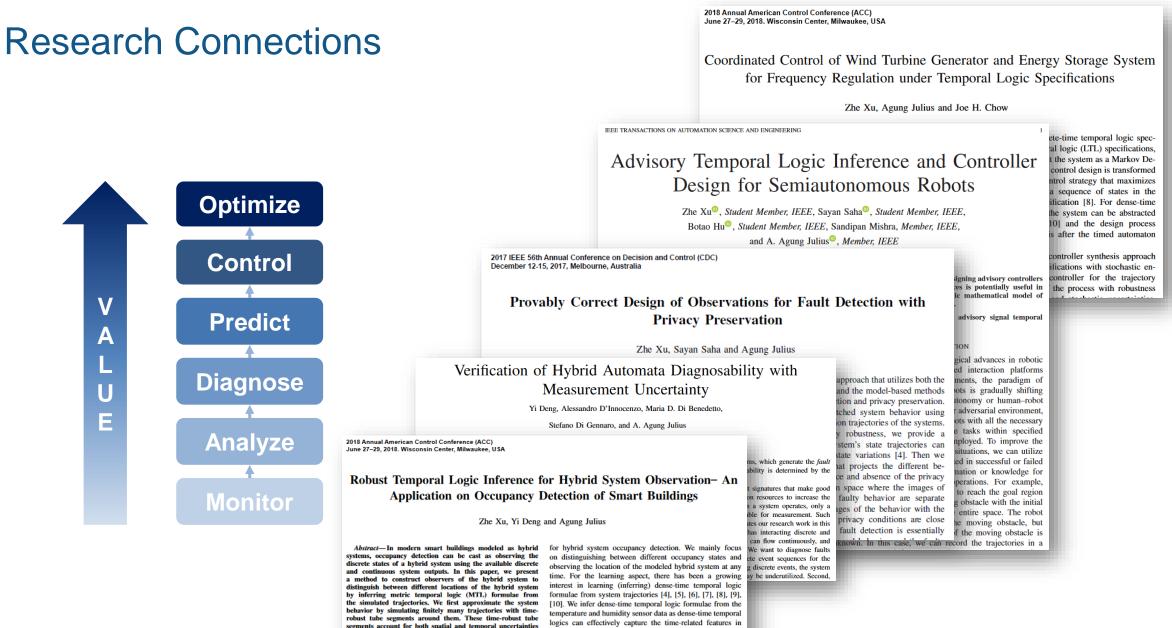
#### Use the computational model (digital twin) during operation

- Optimize fleet or system behavior
- Calculate control setpoints or parameters
- Predict future behavior or events

### Reference example







the transient period when people enter a room. In the

meantime, we also utilize the model information so that

the MTL formula that classifies the finite trajectories we

simulated (or gathered) also classifies the infinite trajectories

that differ from the simulated trajectories by a small margin

in both ensee and time. In our previous work in [11]

that exist in the hybrid system with initial state variations.

The inferred MTL formulae classify different time-robust tube

segments and thus can be used for classifying the hybrid system

behaviors in a provably correct fashion. We implement our

approach on a model of a smart building testbed to distinguish

two cases of room occupancy.

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### References

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### In summary

- Cyber-physical systems continue to gain intelligence and autonomy
- CPS are open, interconnected, and change after deployment
- Formal specification and simulation-based approaches fill an important scalability gap w.r.t formal verification
- Model-Based Design approaches are being supplanted by model-based operation
- Scalability to enterprise-level system will be the value driver



## Thank you!

- Thank you to the RV organizers, Leo, and Bernd
- Dagstuhl on Specification and Verification of CPS
- Jean-Francois Kempf, Khoo Yit Phang, Isaac Ito, and team
- Terri Xiao, Dan Lluch, Jim Tung, Pieter Mosterman, and team



